010,343 TER TO THE OFFICIAL DRAFTSPERSON

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Visvamohan Yegnashankaran et al.

Appln. No.: 10/010,343

Filed: December 05, 2001

For: INTEGRATED CIRCUIT AND METHOD OF FORMING THE INTEGRATED CIRCUIT HAVING A DIE WITH HIGH Q INDUCTORS AND CAPACITORS ATTACHED TO A DIE

WITH A CIRCUIT AS A FLIP CHIP

Group Art Unit: 2826

Examiner: L. Andújar

LETTER TO THE OFFICIAL

DRAFTSPERSON

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313

Dear Sir:

With the permission of the Examiner, please approve the proposed drawing changes shown in red on the attached copy of FIG. 1.

Dated: 5-21-03

espectfully submitted

Mark C. Pickering Registration No. 36,239

Attorney for Assignee

P.O. Box 300

Petaluma, CA 94953-0300

Direct Dial Telephone No. (707) 762-5583

Telephone: (707) 762-5500

Facsimile: (707) 762-5504

Customer No: 33402

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P.O. Box 1450, Alexandria, VA 22313-1450ca 05-21-05

Atty. Docket No. 100-16300 (PO5089)

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